

App. No. 10/658,596  
Amendment Dated: March 10, 2005  
Reply to Office Action of December 10, 2004

**Amendments to the Specification:**

Page 1, Lines 4-5, please amend the paragraph as follows:

~~The present invention relates to electronic circuits and, more particularly, to electronic~~  
Electronic circuits for power regulation.

Page 3, Lines 3-10, please amend the paragraph as follows:

Briefly described, embodiments of the present invention ~~is directed at~~ relate to a circuit that provides a power regulation loop that operates in two modes and uses a single comparator. One mode, an open mode, involves generating an overload signal when the pass transistor is in an oversaturated mode indicating that the voltage drop of the device exceeds a predetermined threshold. The other mode, a linear mode, involves generating an optimal value sense resistor for a liner mode operation of a pass transistor. The two modes do not exist simultaneously and, therefore, a single comparator may be used between the two modes.

Please amend the Abstract as follows:

~~The present invention shows a~~ A circuit that provides a power regulation loop that operates in two modes and uses a single comparator between the two modes. One mode, an open mode, involves generating an overload signal when the pass transistor is in an oversaturated mode indicating that the voltage drop of the device exceeds a predetermined threshold. The other mode, a linear mode, involves generating an optimal value sense resistor for a liner mode operation of a pass transistor. The two modes do not exist simultaneously and, therefore, a single comparator may be used between the two modes.